

AMENDMENTS TO CLAIMS

Claims 1 - 56 (Withdrawn)

Claim ~~57~~ (Previously Amended) A method of producing a series of true random numbers, said method comprising:

using a hardware device to produce an analog noise signal;
converting said analog noise signal to a binary true random sequence of signals;
interfacing said binary true random sequence of signals to a general purpose personal computer; and

utilizing said interfaced binary true random sequence of signals in said computer.

Claim ~~58~~ (Previously Added) A method as in claim ~~57~~ wherein said step of interfacing comprises providing a device driver.

Claim ~~59~~ (Currently Amended) A true random number generator comprising:

a true random number generator circuit for generating a true random sequence of signals; and

21 a personal computer including a ~~means for interfacing with said~~ true random number generator circuit interface, said ~~means for interfacing~~ interface consisting of one or more of the following: a device driver, a TSR, a portion of the operating system of said personal computer, and a program stored in the bios memory of said personal computer.

3 Claim ~~60~~ (Currently Amended) A true random number generator as in claim ~~59~~ wherein said ~~means for interfacing~~ interface comprises a device driver.

3 Claim ~~61~~ (Currently Amended) A true random number generator as in claim ~~59~~ wherein said ~~means for interfacing~~ interface comprises software for testing said true random number generator circuit.

3 Claim ~~62~~ (Currently amended) A true random number generator as in claim ~~59~~ wherein said true random number generator circuit is located in a module separate from said personal computer, and said true random number generator further includes a cable for electrically connecting said module to said personal computer.

3 Claim ~~63~~ (Currently Amended) A true random number generator as in claim ~~59~~ wherein said true random number generator circuit is located on an add-on board for

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mounting in said personal computer.

3 Claim ~~64~~⁶⁵ (Currently Amended) A true random number generator as in claim ~~58~~⁵⁹ wherein said true random number generator circuit is located on the motherboard of said personal computer.

3 Claim ~~65~~⁶⁶ (Currently Amended) A true random number generator as in claim ~~59~~⁶⁰ wherein said true random number generator circuit is located on a peripheral of said personal computer.

El Claim ~~66~~⁶⁷ (Currently Amended) A device for interfacing with a true random number generator, said device comprising:

a personal computer including: a memory means for storing information for interfacing with a true random number generator circuit, and an electronic processor ~~processing means~~ communicating with said memory for interfacing with said true random number generator; and wherein

said information for interfacing with a true random number generator consists of one or more of the following: a device driver, a TSR, a portion of the operating system of said personal computer, and a program stored in the bios memory of said personal computer.